

SANCO 8001 MOTHERBOARD(LOGIC SYSTEMS INT'L INC FPX-290)

	1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16	17	
L	x	74LS138 (decoder 3to8)	74LS08 (quad 2-input AND gate)	74LS20 (2x 4-input NAND gate)	74LS32 (4x OR)	x	7414 (hex inverter, schmitt trigger)	74LS00 (4x 2-input NAND)	74LS109 (2x JnotK flip flop)	74S174 (6x D flip flop)	74LS393 (2x binary counter 4bit)	74S00 (4x 2-input NAND)	74S38 (4x 2-input NAND gate)	x	x	x	x	L
K	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	uPD 4164 (dynamic RAM)	74LS08 (quad 2-input AND gate)	74LS273 (8bit register)	74LS273 (8bit register)	74LS00 (4x 2-input NAND)	74LS10 (3x 3-input NAND)	74LS04 (NOT)	x	PRNT/PCN (DIN 5 pin)	K
J	x	74157 (2to1 multiplexer)	74157 (2to1 multiplexer)	HM 6116 (static RAM 2kByte)			74LS280 (9bit odd-even parity bit generator)	uPD 446 (static RAM 8KByte)			74LS245 (octal bus transceiver, tristate)	TBP28L22M (ROM 2kBit)	74S86 (quad 2-input XOR)	74LS166 (parallel load 8bit shift register)	74LS161 (synchronous presettable 4bit binary counter)	x	DB25 Printer Port	J
H	74LS04 (NOT)	74LS244 (octal buffer)	74LS244 (octal buffer)	2764 EPROM (software: V1.01) (?)			74LS04 (NOT)	uPD 446 (static RAM 8KByte)			74LS273 (8bit register)	2732 EPROM (4kByte) (software character ROM: CGV 7.2)			74LS74 (D edge triggered flip flop)	74LS123 (dual retriggerable monostable multivibrator)		H
F	Extension Bus Pin Header	uPD 780 (Zilog Z80 CPU clone)				74LS244 (octal buffer)	74LS32 (4x OR)	74LS157 (4x 2to1 multiplexer)	74LS157 (4x 2to1 multiplexer)	74LS157 (4x 2to1 multiplexer)	74LS541 (octal buffer gate, tristate)	74LS374 (octal D-type flip flop)	74LS08 (quad 2-input AND gate)	74LS00 (4x 2-input NAND)	x	74LS04 (NOT)	DB25 Communication Port	F
E		uPD 765A (floppy controller)				x	HD46505S / MC6845 (CRT controller)					74LS175 (4x D edge triggered flip flop)	74LS04 (NOT)	74LS74 (D edge triggered flip flop)	74LS32 (4x OR)	74LS32 (4x OR)		E
D		74LS240 (octal buffer)	7406 (inverter)	74LS240 (octal buffer)	74LS51 (AOI gate)	74LS161 (BCD Dec Counter)	Speaker Connector	74LS138 (decoder 3to8)	74LS138 (decoder 3to8)	Z80A-CTC (Z80 counter circuit)			74LS32 (4x OR)	SN75188 (quadruple line driver)	SN75188 (quadruple line driver)	Keyboard connector (DIN 8 pin)		D
C	34 pin header male, floppy connector	7417 (open collector buffer)	(trimmers)	(trimmers)	74LS221 (dual monostable multivibrator)	x	LNE556 (multivibrator)	74LS74 (D edge triggered flip flop)	74LS393 (2x binary counter 4bit)	Z80A-SIO/2 (serial input/output interface)				SN75189 (quadruple line receiver, buffer tristate)	J2 male pin header for daughter board	daughter/mother board connector	C	
B		7417 (open collector buffer)	74LS139 (2to4 decoder)	74LS628 (VCO)	74LS74 (D edge triggered flip flop)	74LS74 (D edge triggered flip flop)	74LS161 (BCD Dec Counter)	74LS74 (D edge triggered flip flop)	74LS04 (NOT)	uPD 8255 (Programmable Peripheral Interface)				x			B	
A	x	x	x	x	x	74LS161 (BCD Dec Counter)	74LS04 (NOT)	x	x	74LS245 (octal bus transceiver, tristate)	74LS541 (octal buffer gate, tristate)	DIP Switch 8bit	74LS38 (NAND open collector)	x	SN75189 (quadruple line receiver, buffer tristate)	daughter board LOGIC SYSTEMS INT'L INC (FPX-290-CN-01)	A	

**daughter board
LOGIC
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(FPX-290-CN-01)**